|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| **LEADLAG- in case of DC INPUT(slow dc with 100Hz & 50Hz frequency)** | | | | | | |
| **Various Detected Faults in LEADLAG in the presence of MULTIPLE faults with deviations** | | | | | | |
| **Injected Faults** | **=500, = 5.1931e-04**  **Optimal Order – 14th** | | **=1000, = 5.1978e-04**  **Optimal Order – 14th** | | **=2000, = 5.2020e-04**  **Optimal Order – 14th** | |
| **No. of Coefficients Out of Bound** | **Fault Detection Status** | **No. of Coefficients Out of Bound** | **Fault Detection Status** | **No. of Coefficients Out of Bound** | **Fault Detection Status** |
| R1 15%**↑**  R2 20% **↓** | 32 | **√** | 32 | **√** | 29 | **√** |
| R2 8% **↓**  R3 12% **↑** | 30 | **√** | 30 | **√** | 29 | **√** |
| R3 20% **↑**  C1 10% **↑**  C2 10% **↓** | 28 | **√** | 27 | **√** | 25 | **√** |
| R1 18% **↓**  C1 10% **↑**  C2 10% **↓** | 34 | **√** | 34 | **√** | 32 | **√** |
| R2 9% **↑**  R3 15% **↓**  C1 12% **↑**  C2 8% **↓** | 32 | **√** | 32 | **√** | 29 | **√** |